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F. No.: GGSIPU/USIC&T/2018/ 9122
Date: 16th April, 2018

B.TECH INTER COLLEGE MAJOR PROJECT COMPETITION - 2018

In order to have a healthy competition among all Engineering Participating Institutions, University School of Information, Communication and Technology shall organize a Major Project Competition at the University Level, involving all Engineering disciplines being run at University Schools and all Affiliated Institutions between **14th May, 2018 (Monday) to 17th May, 2018 (Thursday)**.

The Competition is held with a spirit to attain the following objectives:

1. To improve the quality of Project works being done.
2. To give physical shape to theoretical and practical knowledge
3. To solve real-time problems through creativity, thoroughness and accuracy of conclusions.
4. To promote & find innovative solutions to socially relevant and industry specific problems.
5. To document such events and create a database of all such projects which can help in proper documentation & apply for patents.
6. To provide a forum for healthy competition of presenting original project work done in Affiliated Colleges.

All the Directors/ Principals of affiliated engineering colleges are hereby informed and are requested to allow the final year students of B.Tech programme doing their major projects to participate in this event. For the session 2018-19, **Prof. Abhijit Nayak, Dean, BPIT, Delhi** (Email; nayakabhijit1972@gmail.com, **Mobile no. : +91-9990047456**) has been deputed as the overall Project Coordinator/ Nodal Officer for the conduct of the event and can be contacted for further guidelines & necessary inputs.

Arvinder Kaur
16.4.18
Prof. Arvinder Kaur
(Dean, USIC&T)

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SAMPLE COPY

TOPIC OF THE PROJECT

MAJOR PROJECT REPORT

Submitted in partial fulfilment of the requirements for the award of the degree

of

BACHELOR OF TECHNOLOGY

in

ELECTRICAL & ELECTRONICS ENGINEERING

by

Name of the Student
Enrollment No: XXX

Name of the Student
Enrollment No: XXX

Name of the Student
Enrollment No: XXX

Guided by

Name of the Mentor
Assoc. Professor



DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
BHAGWAN PARSHURAM INSTITUTE OF TECHNOLOGY
(AFFILIATED TO GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY, DELHI)
DELHI – 110089

MAY 2018

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DELHI – 110089

MAY 2018

CANDIDATE'S DECLARATION

It is hereby certified that the work which is being presented in the B. Tech Major Project Report entitled "**NAME OF THE TOPICS**" in partial fulfilment of the requirements for the award of the degree of **Bachelor of Technology** and submitted in the **Department of Electrical & Electronics Engineering** of **BHAGWAN PARSHURAM INSTITUTE OF TECHNOLOGY, New Delhi (Affiliated to Guru Gobind Singh Indraprastha University, Delhi)** is an authentic record of our own work carried out during a period from **January 2018 to May 2018** under the guidance of **Name of the Mentor, Designation.**

The matter presented in the B. Tech Major Project Report has not been submitted by me for the award of any other degree of this or any other Institute.

(Name of the Student)
(En. No: XXXXXX)

(Name of the Student)
(En. No: XXXXXX)

(Name of the Student)
(En. No: XXXXXX)

This is to certify that the above statement made by the candidate is correct to the best of my knowledge. He/She/They are permitted to appear in the External Major Project Examination

(Name of the Mentor)
Designation

Mr. XXXXX
Head, EEED

The B. Tech Minor Project Viva-Voce Examination of **Name of the Student (Enrollment No: XXX)**, has been held on

Project Coordinator

Project Coordinator

(Signature of External Examiner)

ABSTRACT

The abstract is maximum 200 words. It gives the summary of the work.

ACKNOWLEDGEMENT

We express our deep gratitude to **Name of the Mentor**, Designation, Department of Electrical & Electronics Engineering for his valuable guidance and suggestion throughout my project work. We are thankful to **Name of Project Coordinators**, Project Coordinators, for their valuable guidance.

We would like to extend my sincere thanks to **Head of the Department, Name** for his time to time suggestions to complete my project work. I am also thankful to **Name, Principal** for providing me the facilities to carry out my project work.

Note: Students may thank to the persons whom they would like to acknowledge.

Sign
(Name of the Student)
(En. No: XXXXXX)

Sign
(Name of the Student)
(En. No: XXXXXX)

Sign
(Name of the Student)
(En. No: XXXXXX)

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LIST OF ABBREVIATION

SD 1	Sample Distribution System 1
SD 2	Sample Distribution System 2
ANN	Artificial Neural Network
WT	Wavelet Transform
WPT	Wavelet Packet Transform
DWT	Discrete Wavelet Transform
CWT	Continuous Wavelet Transform
ANN	Artificial Neural Network
GT	Gabor Transform
MBWT	M – Band Wavelet Transform
DTCWT	Dual Tree Complex Wavelet Transform
MLP	Multi-Layer Perceptron

LIST OF SYMBOLS

TC_{err} :	Total Classification Error (%)
N_{ms} :	Number Of Misclassified Sample
N_s :	Total Number Of Samples
L_{err} :	Location Error
D_O :	Output Distance Calculated

CHAPTER 1: INTRODUCTION

The student should write the chapters of the B. Tech Project Report in this format. The report may consist of maximum five (05) chapters.

1.0 INTRODUCTION

The student should write introduction of the project.

1.1 Electrical Engineering

The student should write sub- heading and explain the sub – topics in this format.

2.0 MOTIVATION

The motivation of the project includes the literature survey carried out to complete the project.

3.0 OBJECTIVE

The objective of the project should be in bullets.

4.0 SUMMARY OF THE REPORT

Student should write about the content of the report what is included in all chapters.

Chapter 2: It should give the description about the project.

Chapter 3: Results & Discussion

Chapter 4: Conclusion

❖ **Figures Should Be Represented in the following format:**

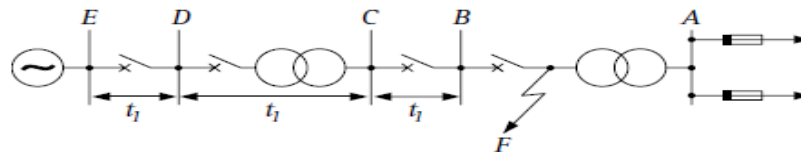


Figure 1.4: Radial Distribution System.

❖ **Tables Should Be Represented in the following format:**

TABLE 1.1: Zone-wise Categorization of Sample Distribution 1

Zone	Nodes
Zone 1	1 - 4
Zone 2	5, 6 & 13

REFERENCES

- [1] M. M. Saha, Ratan Das, et-al, "Review of fault location techniques for distribution systems," Power Systems and Communications Infrastructures for the future," Beijing, pp.1-6. Sept. 2002.
- [2] (Basic Book/Monograph Online Sources) J. K. Author. (year, month, day). Title (edition) [Type of medium]. Volume (issue). Available: [http://www.\(URL\)](http://www.(URL))
- [3] Letter Symbols for Quantities, ANSI Standard Y10.5-1968.